

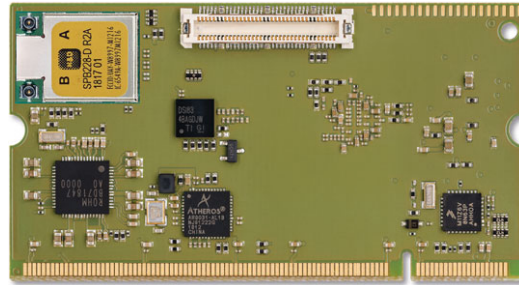
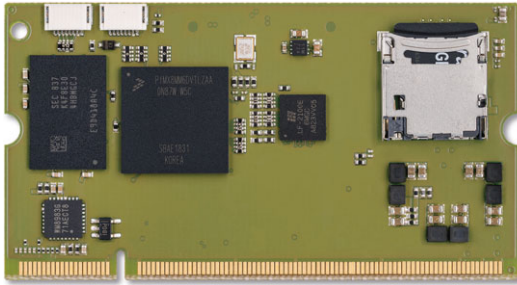
Trizeps VIII Mini

NXP i.MX 8M Mini SODIMM-200 CPU-Module

Keith & Koep

Additional features to i.MX 8M Mini CPU:

- Configurable FPGA, e.g. with MIPI to RGB converter
- Additional Cortex M0+ MCU with e.g. CAN, ADC, SPI, I2C
- Onboard WLAN/Bluetooth module (optional)
- LVDS (Single or Dual) Transceiver
- 1 GBit Ethernet, USB 3.0
- Trizeps SODIMM-200 compatible, compact and scalable



Variable Features

Processor	NXP™ i.MX 8M Mini with Arm® Quad-Core Cortex A53 at up to 1.8 GHz (consumer), 1.6 GHz (industrial), additional Cortex M4
MCU	NXP™ Kinetis V Arm® Cortex-M0+ at up to 75MHz / 8x 16 Bit ADC, CAN, UART, SPI, GPIO etc.
RAM Memory	Up to 4 GByte LPDDR4-3200, 32 Bit
Flash Storage	Onboard 8 Bit wide eMMC (standard) or onboard 4 Bit wide µSD Card Socket (on request) – not available at the same time
FPGA	Programmable FPGA with up to 4300 LUTs to convert parallel display/camera/data-streams to MIPI DSI/CSI (optional)
Wireless Communication (on request)	Onboard WLAN-Bluetooth module, WLAN 2.4GHz/5GHz, 802.11 a/b/g/n/ac 2x2 MU-MIMO, Bluetooth 4.2 / 5.0

Onboard Features

Display Interfaces	MIPI display (4ch), Single-, Dual-LVDS or LCD 24 Bit RGB
Interfaces	2x USB2.0 OTG, PCIe, 4Bit wide SDIO, SPDIF In/Out, I2S, Multichannel Serial-Audio-Interface, 4 UARTs, 2x I2C, SPI, QSPI, GPIOs, PWM
Ethernet	Onboard 10/100MBit/1GBit RGMII PHY and SIOP interface
Extension Connector	Additional FX11 60pol. high-speed board to board connector
Camera Interfaces	8bit parallel, MIPI (4ch)
Audio Codec	Stereo Headphone output, Mono Speaker output, Stereo Line-In, Microphone input
Power	High-Eff. PMIC with single supply controlled by I2C
Pin compatible	Pin compatible to Trizeps SODIMM-200 CPU modules

General Details

System Software	Linux Kernel 4.14, Android 9, Windows 10 IoT Core (coming soon)
Voltage Supply	+3V3 DC
Operating Temperature	-25°C to +85°C (industrial)
Board Dimensions	67.6 x 36.7 x 6.4 mm incl. µSD Socket (W x H x D)
Environmental Standard	RoHS, REACH, WEEE
Availability	10 years form, fit, function*



*beginning product life circle Rev.1904
Technical modifications reserved, errors excepted

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