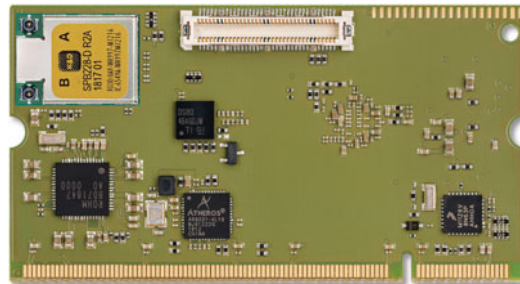
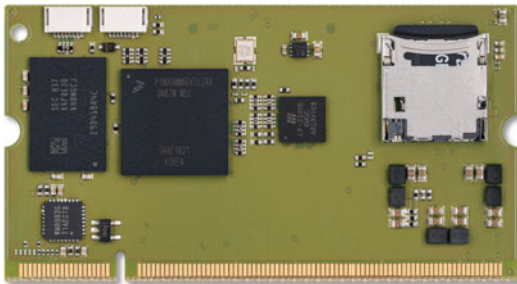


Trizeps VIII Mini

SODIMM-200 CPU-Module with NXP i.MX 8M Mini

Keith & Koep

- NXP i.MX 8M Mini Arm Cortex A53 CPU up to 1.8 GHz, with integrated Cortex M4
- Configurable FPGA (up to 4300 LUTs), e.g. with MIPI to RGB converter
- NXP Kinetis V Arm Cortex M0+ MCU provides additional interfaces like CAN, ADC, SPI, I2C
- Up to 8 GByte LPDDR4-3200 RAM, 32 Bit
- 1 GBit Ethernet, USB 2.0
- Onboard WiFi/Bluetooth module
- LVDS Transceiver (Single or Dual)
- Additional Hi-Fi Audio Codec
- Low power consumption via 14nm LPC FinFET technology



Features

Processor	NXP™ i.MX 8M Mini with Arm® Cortex A53 Quad-Core up to 1.8 GHz (consumer), 1.6 GHz (industrial), with integrated Cortex M4
MCU (optional)	NXP™ Kinetis V Arm® Cortex-M0+ up to 75 MHz / CAN, 8x 16 Bit ADC, UART, SPI, GPIO, I2C etc.
RAM Memory	Up to 8 GByte LPDDR4-3200, 32 Bit
Flash Storage	Onboard 4 Bit wide µSD Card Socket (recommendation) or onboard 8 Bit wide eMMC – not available at the same time
FPGA (optional)	Programmable FPGA with up to 4300 LUTs to convert parallel display/camera/data-streams to MIPI DSI/CSI
Wireless Communication (optional)	Onboard WiFi/Bluetooth module, WiFi 2.4 GHz / 5GHz, 802.11 a/b/g/n/ac 2x2 MU-MIMO / Bluetooth 4.2, 5.0 / External chipsets for wireless communication can be connected via SDIO, PCIe or USB interfaces
Display Interfaces	MIPI display (4 channel) / Single-, Dual-LVDS or LCD 24 Bit RGB
Interfaces	2x USB2.0 OTG, PCIe (routed to SODIMM socket or connected to WiFi-Bluetooth module), 4 Bit wide SDIO, SPDIF In/Out, I2S, Multichannel Serial-Audio-Interface, 4x UART, 2x I2C, SPI, QSPI, GPIOs, PWM
Ethernet	Onboard 10/100MBit/1GBit RGMII PHY and SIOP interface
Extension Connector	Additional FX11 60 pol. high-speed board to board connector
Camera Interfaces	8bit parallel, MIPI (4 channel)
Audio Codec	Stereo Headphone output, Mono Speaker output, Stereo Line-In, Microphone input
Power	High-Eff. PMIC with single supply controlled by I2C
Pin compatible	Pin compatible to Trizeps SODIMM-200 CPU modules

General Details

System Software	Linux Kernel 4.14, Android 9, Windows 10 IoT Core
Voltage Supply	+3V3 DC
Operating Temperature	-40 bis 85°C (industrial) / -25 bis 85°C (Extended Consumer) / 0 bis 70°C (Consumer)
Board Dimensions	67.6 x 36.7 x 6.4 mm (W x H x D)
Environmental Standard	RoHS, REACH, WEEE
Availability	10 years form, fit, function*



*beginning product life circle
Technical modifications reserved, errors excepted

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Trizeps VIII Mini

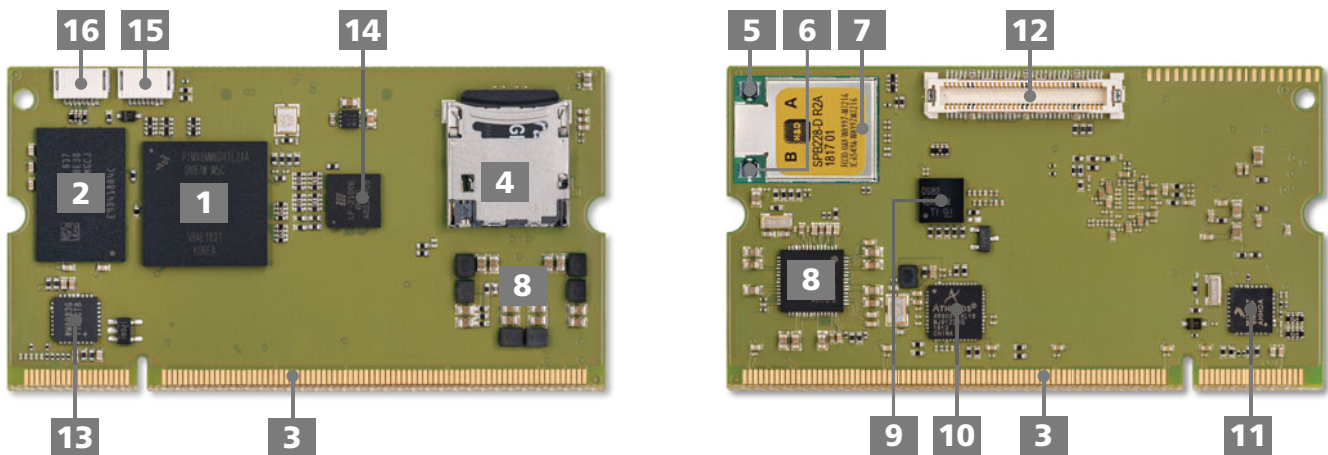
SODIMM-200 CPU-Module with NXP i.MX 8M Mini

Keith & Koep

The new Trizeps VIII Mini CPU Module from the NXP i.MX8 series uses the i.MX 8M Mini processor, which provides the high performance demands of current video, voice, and audio processing, including video resolutions up to 1080p. The NXP iMX8M Mini CPU is produced in the 14nm LPC FinFET technology, which enables high operating frequencies in combination with a low power consumption.

The Trizeps VIII Mini adds further features with the configurable FPGA, the programmable NXP Kinetis Cortex M0+ MCU, the LVDS transceiver, the Hi-Fi Audio Codec and the onboard WiFi/Bluetooth module. This integrates e.g. CAN, ADC, SPI, I2C interfaces as well as RGB and LVDS displays in addition to the iMX8M Mini CPU functionality.

Therefore, the Trizeps VIII Mini is the ideal CPU module for typical embedded solutions such as industrial/home automation, robotics, automotive, medical, aerospace, retail and is also ideally suitable for current streaming, audio applications, modern imaging devices or handhelds as well as battery-powered solutions.



Features

- | | |
|---|--|
| 1 NXP i.MX 8M Mini Arm Cortex A53 CPU up to 1.8 GHz (Quad-Core), integrated Cortex M4 | 8 Power Management IC (PMIC) |
| 2 Up to 8 GByte LPDDR4-3200 RAM, 32Bit | 9 Single or Dual LVDS Transceiver |
| 3 SODIMM 200 Connector
Interfaces: UART (4x), SPI, SDIO, QSPI, I2C (3x), GPIO, PWM, USB 2.0 (2x), Headphone (stereo), Line-In (stereo), Microphone, Speaker, SPDIF In/Out, Serial Multichannel Audio, PCM, PCIe (routed to SODIMM socket or connected to WiFi-Bluetooth module), FPGA configurable pins (e.g. display RGB16/18/24, SPI/UART/GPIO, parallel camera), Kinetis MCU Pins (e.g. CAN, ADC 16 bit, UART, SPI), Ethernet (1 Gbit, 100/10 Mbit), +3V3 power supply | 10 1 Gbit Ethernet Phy |
| 4 Socket for µSD cards (4 Bit, lockable, recommendation) or eMMC (8 Bit) – not available at the same time | 11 NXP Kinetis MCU, Cortex M0+ (optional) |
| 5 WiFi / Bluetooth UFL Coax connector | 12 FX11 High-Speed board-to-board connector, 60 pole
Interfaces: Mipi DSI (4 channel), Single or Dual LVDS, 1x Mipi CSI (4 channel), Ethernet SIOF |
| 6 WiFi / Bluetooth UFL Coax connector | 13 Hi-Fi Audio Codec |
| 7 WiFi-Bluetooth Module (optional):
802.11 a/b/g/n/ac 2x2 MU-MIMO (2,4 GHz, 5 GHz) / Bluetooth 4.2, 5.0 | 14 FPGA up to 4300 LUTs (optional) |
| | 15 JTAG (i.MX 8M Mini CPU) |
| | 16 JTAG (FPGA and MCU) |

SODIMM Standard

The Trizeps VIII Mini includes the Keith & Koep SODIMM 200 Standard, the world's longest existing SODIMM standard. Like no other module standard, the SODIMM 200 ensures the pin compatibility of the Trizeps modules. The main difference of the Trizeps VIII Mini is the missing address/data bus, which is no longer supported by the i.MX 8M Mini CPU. The pins, which are no longer needed for this function, are used for GBit Ethernet signals. Trizeps VIII Mini, Trizeps VIII Nano and Trizeps VIII are mostly pin compatible.



*beginning product life circle
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